

EE 1000 Tentative Course Schedule – Fall 2013 (MW)

Date	Synopsis	Homework
8/26	Syllabus, Introduction, Types of Engineers, Engineering vs. Science vs. Technology	Worksheet 1
8/28	Scientific Notation, Precision, Dimensions, Units, Electric Terms, DC v. AC	Worksheet 2
9/2	Labor Day	
9/4	Resistance, Ohms Law, Schematics, Color Code, Series/Parallel	Worksheet 3
9/9	Lab Equipment: Multi-meters, Power Supplies, Function Generators, Oscilloscopes. Lab rules, ESD	Lab Exercise 1
9/11	*Lab – Using lab equipment	
9/16	Capacitance, Inductance, Resonance	Worksheet 4
9/18	Electromagnetic Radiation, Amplitude Modulation, Crystal Radio	Worksheet 5
9/23	*Lab - Project 1 – Crystal Radio	
9/25	Making Schematic Diagrams (plus Lab if time permits)	Worksheet 6
9/30	*Lab - Project 1 – Pass-off (Schematic Due)	
10/2	Prototype Board. Diodes, Transistors, FETs and op-amps. Oscillator for Project 2 (Theremin), Photo-resistors	Worksheet 7
10/7	*Lab - Project 2 – Build Variable Oscillator	
10/9	Analog Signals, Fourier Analysis, Filters (passive and active), Amplifiers, variable gain. Speakers.	Worksheet 8
10/14	*Lab - Project 2 – Build Filter and Amplifier	
10/16	Digital vs. Analog, Binary, Hexadecimal	Worksheet 9
10/21	*Lab – Project 2 – Pass-off (Schematic Due)	
10/23	Counters, NAND gates, NOR gates, LEDs	Worksheet 10
10/28	Analysis v. Synthesis, Tools, Project 3 (Electric Die), Soldering	Lab Exercise 2
10/30	*Lab – Project 3 – Simulate the Electric Die with MultiSim	
11/4	*Lab – Project 3 – Assemble the Electric Die	
11/6	*Lab – Project 3 – Finish and Pass-off	
11/11	Linear power supplies (Transformer, rectifier, regulator)	Worksheet 11
11/13	Volt Meter – A/D converters, Comparitors	Worksheet 12
11/18	*Lab – Project 4 – Build Voltage Regulator & Comparitor	
11/20	Volt-Meter – Encoder, 7-segment displays, 7447	Worksheet 13
11/25	*Lab – Project 4 – Build Encoder	
11/26	*Lab – Project 4 – Finish Voltmeter	
12/2	†Motor Drive Circuits, Control systems, PID control	†Worksheet 14
12/4	*Lab – Project 4 – Pass-off (Schematic Due)	
Final		

* Meet in the Lab on these days

† Time permitting